EEE104 Circuit Analysis I

Ankara University

Faculty of Engineering

Electrical and Electronics Engineering Department

Ankara University Electrical and Electronics Eng. Dept. EEE104

Circuit Elements

EEE104 Circuit Analysis I

Lecture 3

Agenda

- Circuit Model
- Kirchhoff's Laws

• A Circuit Model with a Switch









• Kirchhoff's Current Law

For a node;

(a) Total of incoming currents are zero [Incoming (+), outgoing (-)]

or

(b) Total of outgoing currents are zero [Incoming (-), outgoing (+)]

or

(c) Total of incoming currents equals total of outgoing currents [Incoming (+), outgoing (+)]

- Node a: $-i_1 i_s = 0$
- Node b: $i_1 + i_2 = 0$
- Node c: $-i_2 + i_3 = 0$
- Node d: *i*_s-*i*₃=0

[Considering (a)]

• Kirchhoff's Voltage Law

Total of voltages in a mesh (closed loop) is zero $v_3+v_2-v_1-v_s=0$

Reference

 Electric Circuits, Tenth Edition, James W. Nilsson, Susan A. Riedel Pearson, 2015