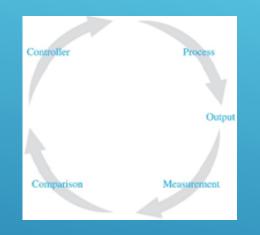
# CONTROL SYSTEMS



#### Doç. Dr. Murat Efe



## **Root Locus Analysis - Summary Rules for Constructing Root Loci**

- **1. Locate the open loop poles and zeros**
- **2. Determine the loci on the real axis**
- **3. Determine the asymptotes of root loci**
- 4. Find the breakaway and break-in points
- 5. Determine the angle of departure from a complex pole
- 6. Determine the angle of arrival at a complex zero
- 7. Find the point where the root loci may cross the imaginary axis
- 8. Determine the shape of the root loci in the broad neighborhood of the j<sub>0</sub> axis and the origin of the s-plane
- **9. Determine the closed loop poles**

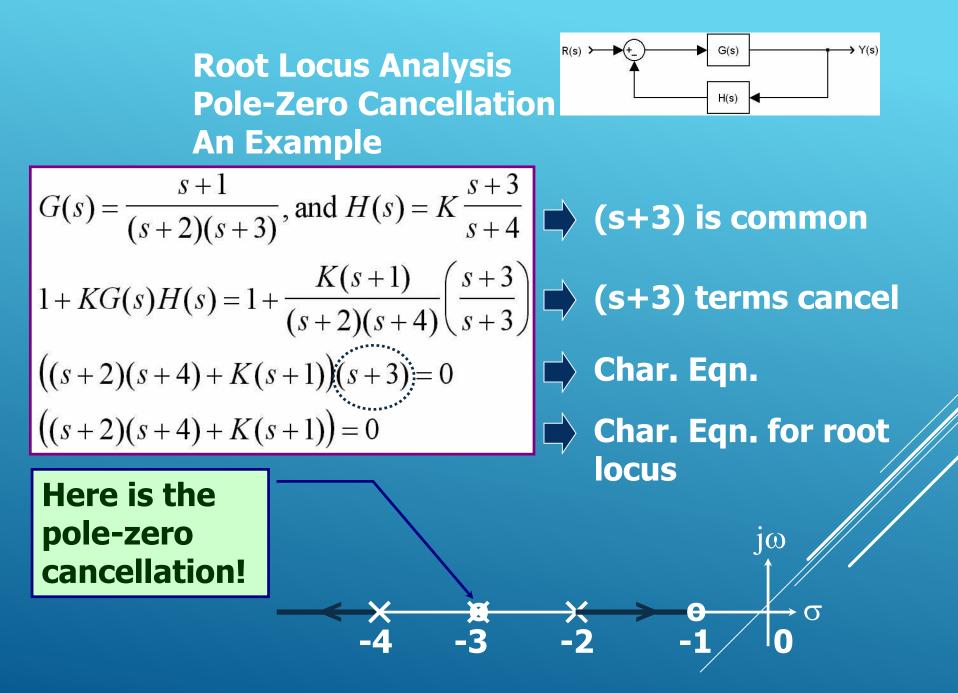
Root Locus Analysis  
Pole-Zero Cancellation  

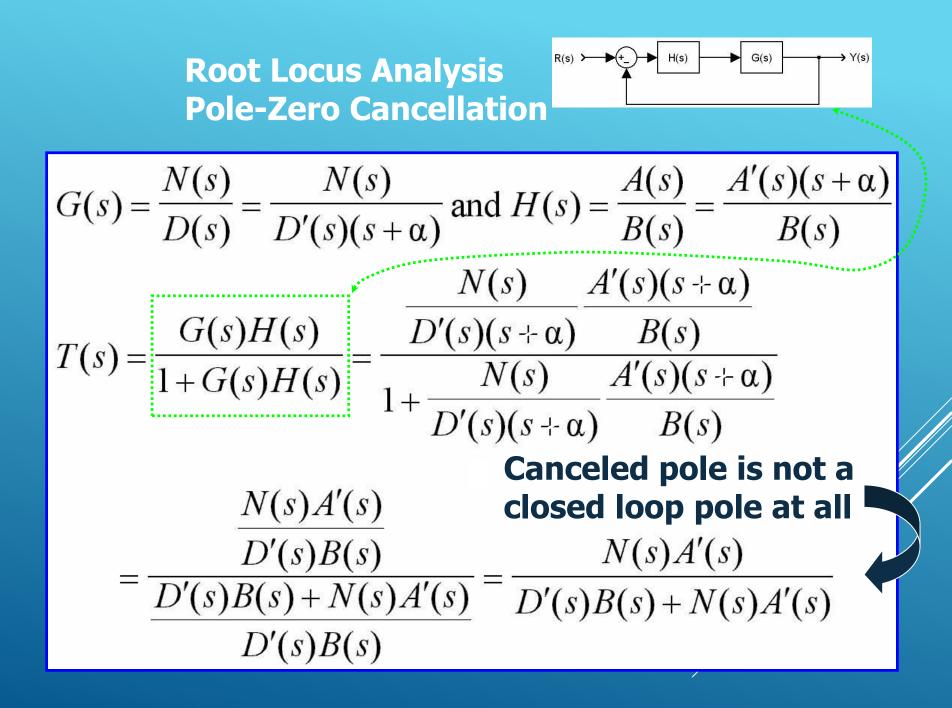
$$G(s) = \frac{N(s)}{D(s)} = \frac{N(s)}{D'(s)(s+\alpha)} \text{ and } H(s) = \frac{A(s)}{B(s)} = \frac{A'(s)(s+\alpha)}{B(s)}$$

$$\overline{T(s)} = \frac{G(s)}{1+G(s)H(s)} = \frac{\frac{N(s)}{D'(s)(s+\alpha)}}{1+\frac{N(s)}{D'(s)(s+\alpha)}} \frac{A'(s)(s+\alpha)}{B(s)}$$

$$= \frac{\frac{N(s)}{D'(s)(s+\alpha)} \frac{B(s)}{B(s)}}{\frac{D'(s)B(s)+N(s)A'(s)}{D'(s)B(s)}} = \frac{\frac{N(s)B(s)}{D'(s)B(s)}}{\frac{D'(s)B(s)+N(s)A'(s)}{D'(s)B(s)}} \frac{1}{(s+\alpha)}$$

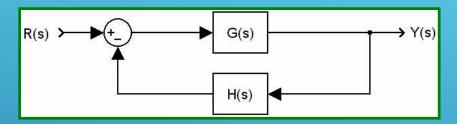
$$= \frac{N(s)B(s)}{D'(s)B(s)+N(s)A'(s)} \frac{1}{(s+\alpha)} \sqrt{\frac{\text{Canceled pole of G(s)}}{\text{is kept as a CL pole!}}}$$



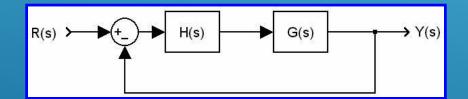


Root Locus Analysis  
Pole-Zero Cancellation  
An Example (Same result is obtained!)  
$$G(s) = \frac{s+1}{(s+2)(s+3)}, \text{ and } H(s) = K \frac{s+3}{s+4} \implies (s+3) \text{ is common}$$
$$1+KG(s)H(s) = 1 + \frac{K(s+1)}{(s+2)(s+4)} \begin{pmatrix} s+3\\ s+3 \end{pmatrix} \implies (s+3) \text{ terms cancel}$$
$$(s+2)(s+4) + K(s+1)(s+3) = 0$$
$$((s+2)(s+4) + K(s+1))(s+3) = 0$$
$$((s+2)(s+4) + K(s+1)) = 0$$
$$Here \text{ is the pole-zero cancellation!}$$

## **Root Locus Analysis Pole-Zero Cancellation**



**Canceled pole** <u>is a CL</u> pole **Root locus does not notice it** 



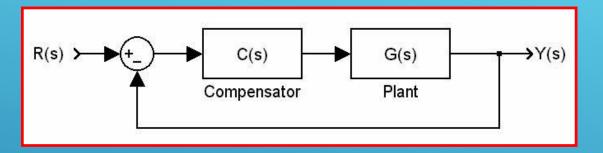
Canceled pole <u>is NOT a CL</u> pole Root locus does not notice it

#### P-5 Design based on Root Locus

The goal is to meet the design specifications, and the way we followed so far has been to modify the gain K. What if this is not sufficient?

Modify the system dynamics suitably to obtain the desired result, which means compensation, and the device you used is called compensator.

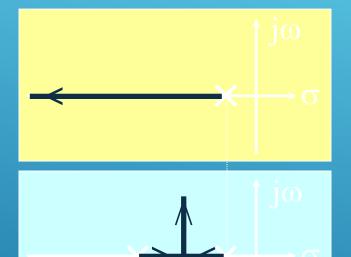
#### Design based on Root Locus Description of the Compensation Problem



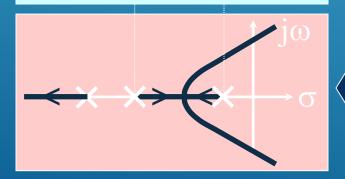
C(s) may remove some poles of G(s) and may add new poles, or C(s) may remove some zeros of G(s) and may add new zeros to change the shape of root locus.

Once the shape of root locus becomes suitable to locate the desired closed loop poles, the adjustment of loop gain K is performed.

## **Design based on Root Locus Effects of Addition of Poles**

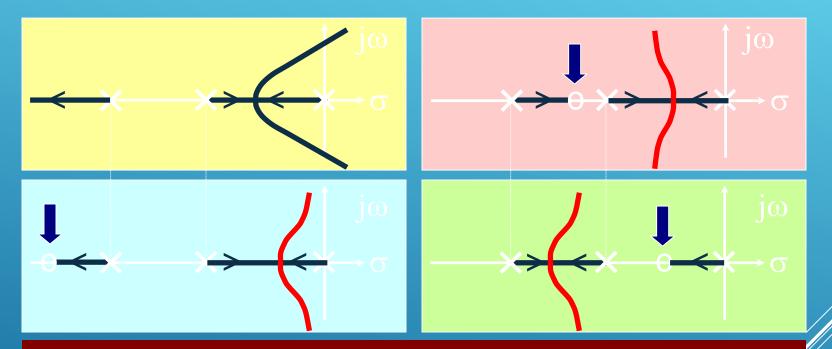


Adding poles pulls the root locus to the right



## After some value of K, two of the CL poles are unstable!

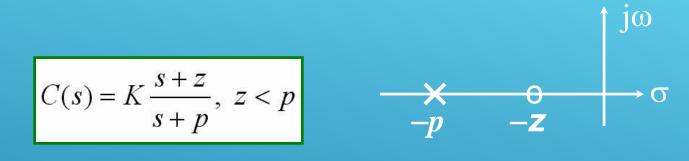
## Design based on Root Locus Effects of Addition of Zeros



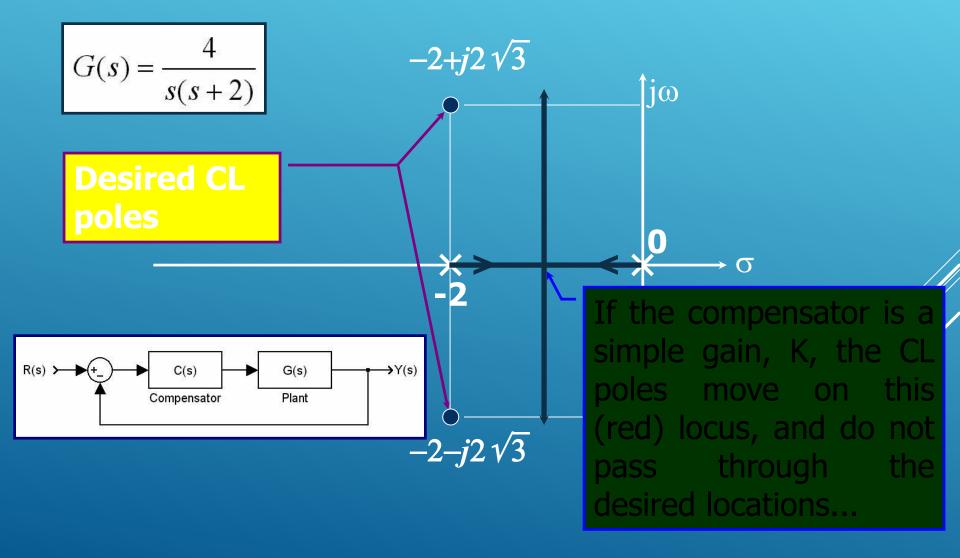
Adding zeros pulls the root locus to the left

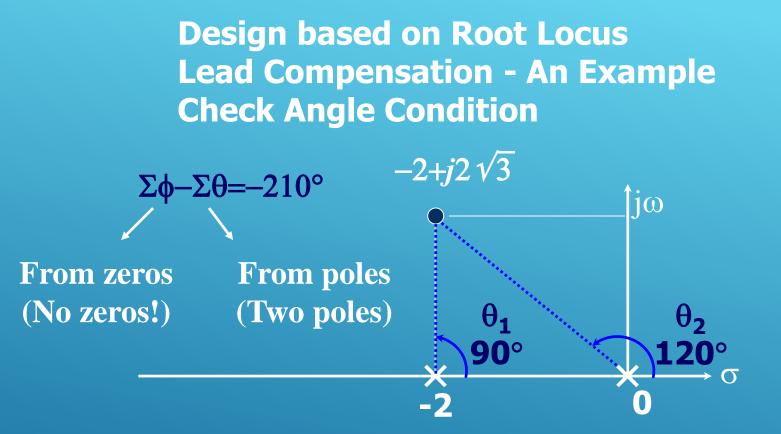
Notice that, the CL poles are always stable for this example. Adding zeros increase the stability of the CL system, this is due to the anticipatory behavior of the derivative action.

#### **Design based on Root Locus Lead Compensation**

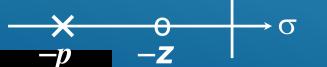


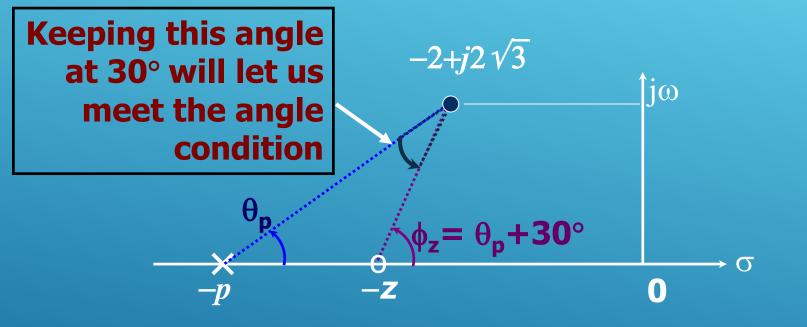
Calculate the angle deficiency ( $\phi$ ) at given locations, which are the desired CL pole locations, and then locate p and z to provide the  $-\phi$  to satisfy angle condition. Then calculate K from the magnitude condition.



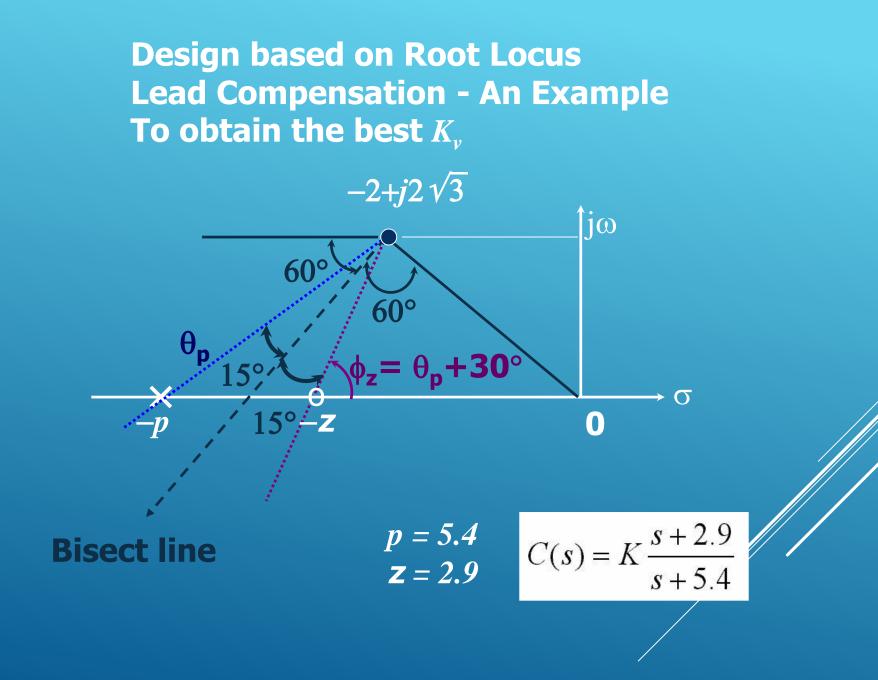


Compensator must provide 30° to satisfy the angle condition. Remember the p & z configuration of the lead compensator.  $\uparrow j\omega$ 





Clearly, there are lots of configurations providing 20° angle contribution? Which one should we choose?



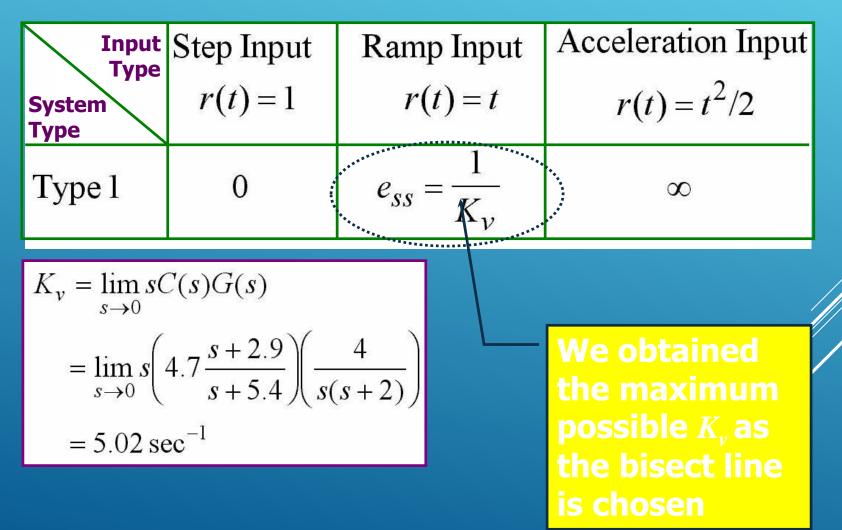
## Design based on Root Locus Lead Compensation - An Example Determine K from the Magnitude Condition

$$|C(s)G(s)|_{s=-2\pm j2\sqrt{3}} = 1$$

$$|K\frac{s+2.9}{s+5.4}\frac{4}{s(s+2)}|_{s=-2+j2\sqrt{3}} = 1$$

$$(k = 4.7, C(s) = 4.7\frac{s+2.9}{s+5.4}$$

## Design based on Root Locus Lead Compensation - An Example Static Velocity Error Constant



## Design based on Root Locus Lead Compensation More general case: You are specified K<sub>v</sub>

$$K_{v} = \lim_{s \to 0} sC(s)G(s) = \lim_{s \to 0} s\left(K\frac{s+z}{s+p}\right)\left(\frac{4}{s(s+2)}\right) = \frac{2Kz}{p}$$

$$\phi_{z} - \theta_{p} = 30^{\circ} = \arctan\left(\frac{2\sqrt{3}}{z-2}\right) - \arctan\left(\frac{2\sqrt{3}}{p-2}\right)$$

$$\left|K\frac{s+z}{s+p}\frac{4}{s(s+2)}\right|_{s=-2+j2\sqrt{3}} = 1 \Rightarrow K = 2\sqrt{3}\sqrt{\frac{\left(p-2\right)^{2}+12}{\left(z-2\right)^{2}+12}}$$
Angle Condition
Magnitude Condition
Solve the three equations for *z*, *p* and *K*

**Design based on Root Locus Lead Compensation - Remarks** 

You have been given the CL poles explicitly in this example. In a more realistic problem, several specifications imply them. For example, the transient or steady state characteristics are described and you find out the required CL poles.

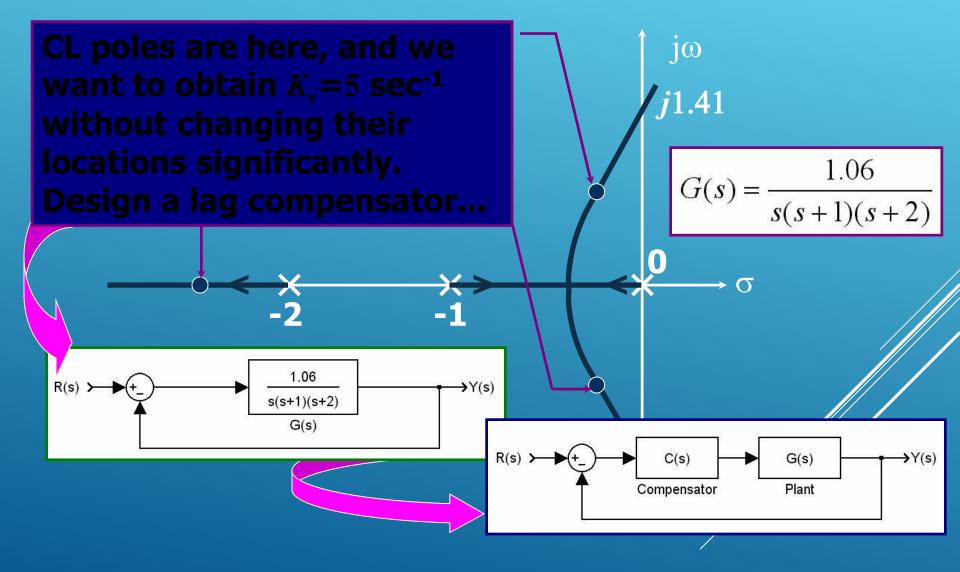
Before jumping into equations, roughly sketch the root loci and make sure that you are on the right way.

#### **Design based on Root Locus** Lag Compensation

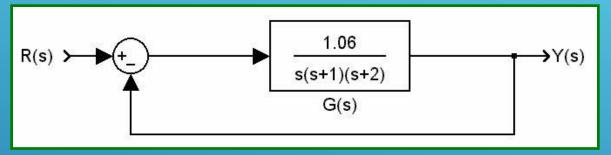


If the system performs well during transient period but poor during steady state, use a lag compensator to improve the steady state characteristics. Lag compensator increases the loop gain without modifying the locations of the dominant CL poles significantly. This is true as long as you locate *p* and *z* close to each other, furthermore, both are located close to origin. **Design based on Root Locus** Lag Compensation

Typically, a desired static error constant is given. Since the angle contribution of the lag compensator is very small, the root loci does not change significantly. If this is not the case, i.e. if transient response is not satisfactory either, then you will be using a lag-lead compensator, which will be considered later...



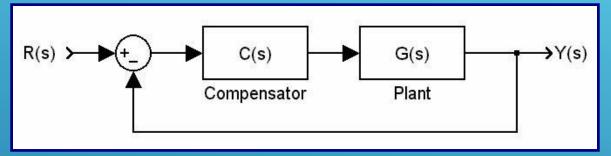
## With this configuration,



- The dominant CL poles are at  $s=-0.3307 \pm j0.5864$ 
  - The damping ratio is ζ=0.491
     The static velocity error constant is K<sub>ν</sub> = 0.53 sec<sup>-1</sup>



## Adopt this configuration,



#### Locate z and p very close to origin

$$C(s) = K \frac{s+z}{s+p}, \ z > p$$

$$G(s) = \frac{1.06}{s(s+1)(s+2)} \qquad K_v = \lim_{s \to 0} sG(s) = 0.53$$
$$C(s)G(s) = K \frac{s+z}{s+p} \frac{1.06}{s(s+1)(s+2)}$$
$$K_{v\text{NEW}} = \lim_{s \to 0} sC(s)G(s) = K \frac{z}{p} 0.53$$

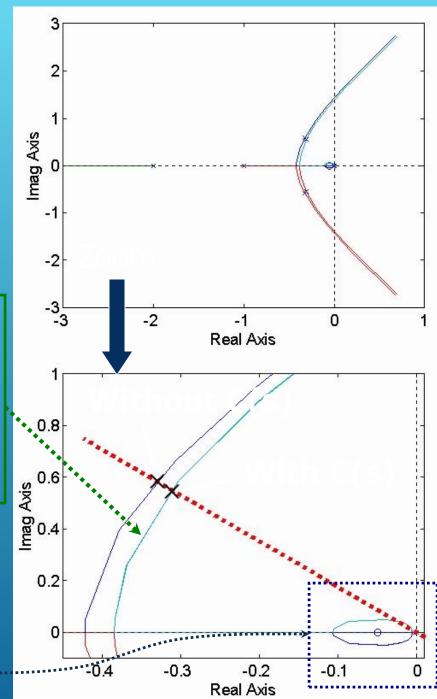
K<sub>vNEW</sub>/K<sub>v</sub>≅10, so set z=0.05 and p=0.005
Calculate angle contribution, which is ≈4°
This will slightly change the root locus
Tune K to keep ζ same (ζ=0.491), K=1.0235

Design, R-Locus Lag Comp. Example

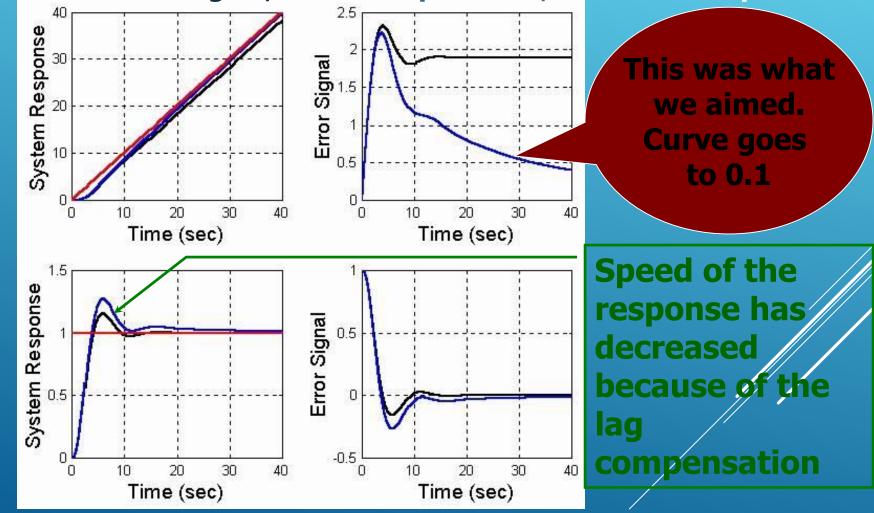
• What would happen if there were no K adjustment?

The answer is on the graph. Here you see two loci, which are almost identical. Nevertheless, you have to find the correct value of K...

Pay attention, the pole and the zero of C(s) are here



Red: Command Signal, Blue: Compensated, Black: Uncompensated

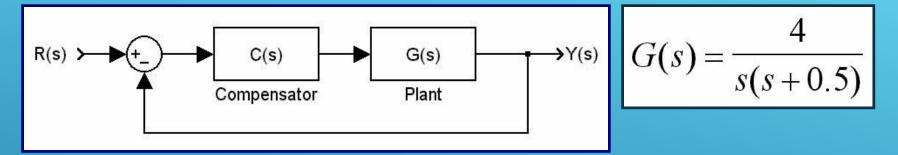


**Design based on Root Locus Lag-Lead Compensation** 

- Lead compensation speeds up the response and increases the stability of the system.
- Lag compensation improves the steady state accuracy but reduces the speed of the response.

If the design specifications require both a fast response and better steady state characteristics, a Lag-Lead compensator is used. **Design based on Root Locus Lag-Lead Compensation** 

• Calculate the relevant variables ( $\omega_n$ ,  $\zeta$ ,  $\omega_d$  etc) Firstly, design the Lead Compensator Calculate the angle deficiency Output to the series of the compensator Output the pole such that the angle condition is met Secondly, design the Lag Compensator Output to the second According to steady state response specs. locate the zero Check the angle contribution of Lag Comp  $\odot$  If necessary, retune the gain so that  $\zeta$  is kept at its desired value.



#### **Design Specifications**

Dominant CL poles are desired to have ζ = 0.5
Desired Undamped natural frequency is ω<sub>n</sub> = 5 rad/sec
Desired Static velocity error constant

is  $K_v = 80 \text{ sec}^{-1}$ 

Design based on Root Locus Lag-Lead Compensation - An Example Step 1: Calculate the relevant variables If there is no compensator, you have

$$G(s) = \frac{4}{s(s+0.5)}$$

$$T(s) = \frac{G(s)}{1+G(s)} = \frac{4}{s^2+0.5s+4}$$

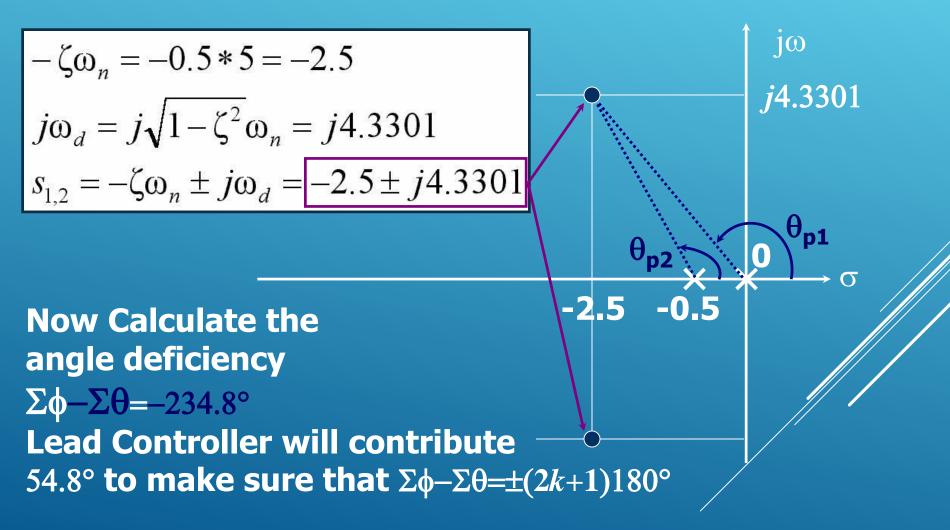
$$\zeta = 0.125 \text{ and } \omega_n = 2 \text{ rad/sec}$$

$$K_v = \lim_{s \to 0} sG(s) = 8 \text{ sec}^{-1}$$

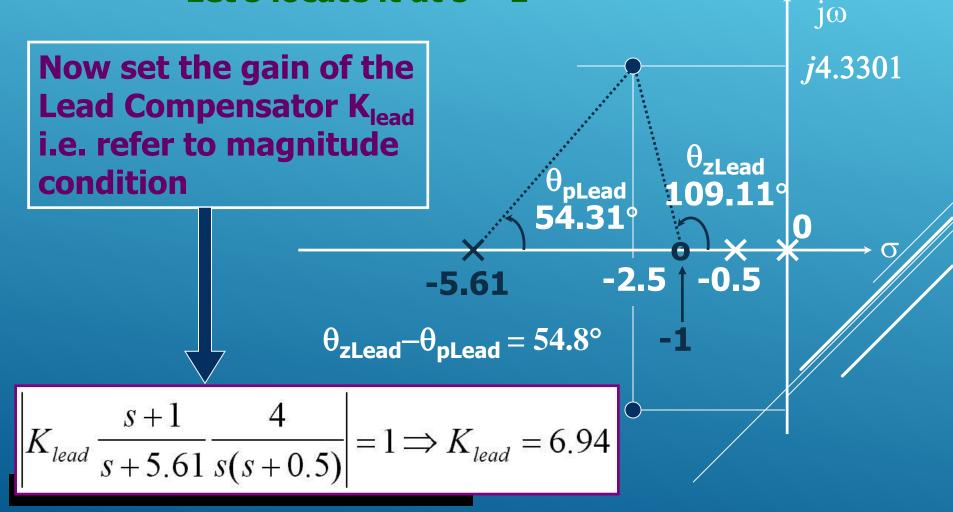
$$Desired$$

$$\omega_n = 5 \text{ rad/sec}$$

Design based on Root Locus Lag-Lead Compensation - An Example Step 2: Design the Lead Compensator



Design based on Root Locus Lag-Lead Compensation - An Example Step 2: Locate the zero of Lead Comp. Let's locate it at s=-1



Design based on Root Locus Lag-Lead Compensation - An Example Step 3: Design the Lag Compensator

$$C_{lead}(s)G(s) = \left(6.94\frac{s+1}{s+5.61}\right) \left(\frac{4}{s(s+0.5)}\right)$$
$$K_{v(new)} = \lim_{s \to 0} sC_{lead}(s)G(s) = 9.9 \text{ sec}^{-1}$$

$$\frac{K_{v(new)}}{K_{v(new)}} = \lim_{s \to 0} sC_{lead}(s)G(s) = 9.9 \text{ sec}^{-1}$$
$$\frac{K_{v(new)}}{K_{v(desired)}} = \frac{9.9 \text{ sec}^{-1}}{80 \text{ sec}^{-1}} = 0.124$$

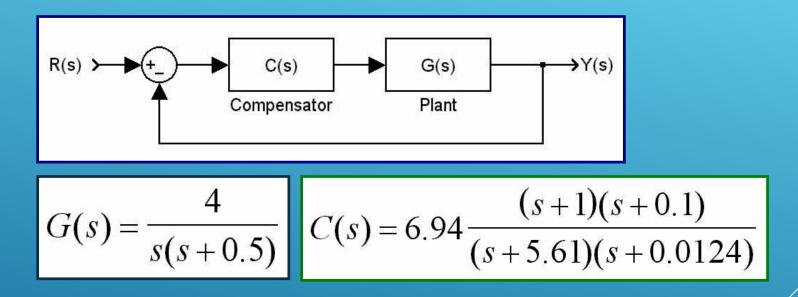
When s=0, the Lag compensator must // increase the loop gain by  $1/0.124 \approx 8.06$ 

Design based on Root Locus Lag-Lead Compensation - An Example Step 3: Locate the zero of Lag Compensator Let's locate it at s=-0.1  $C_{lag}(s) = K_{lag} \frac{s+0.1}{s+0.0124}$ 

$$C_{lead}(s)C_{lag}(s)G(s) = \left(6.94\frac{s+1}{s+5.61}\right) \left(K_{lag}\frac{s+0.1}{s+0.0124}\right) \left(\frac{4}{s(s+0.5)}\right)$$
$$K_{v} = \lim_{s \to 0} sC_{lead}(s)C_{lag}(s)G(s) = K_{lag}79.8114 \text{ sec}^{-1}$$

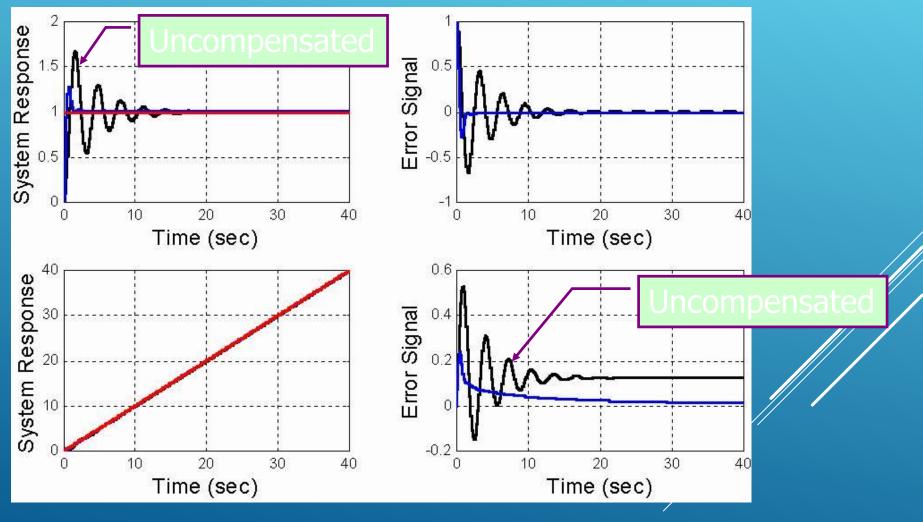
Angle Contribution is : 0.8791°

Angle contribution is acceptably small. However, this has slightly changed z. A very tiny tuning can be made if the design specifications are too stringent. For this example, there is no need to do so, keep  $K_{lag} = 1$ .



## Now, test and see whether the design specifications are met or not...

## Design based on Root Locus Lag-Lead Compensation - An Example Step and Ramp Responses



## **Design based on Root Locus Lag-Lead Compensation - An Example A** Comparison

Simple K<sub>lag</sub>

 $= 79.81139669944224 \text{ sec}^{-1}$ ζ = 0.49452458450471 **Good enough** CL Poles: s=-2.44946613086810 ± *j*4.30511842727874 s=-1.12268288809756 and s=-0.10078485016624

#### K<sub>v</sub> is exact

 $Kv = 80 sec^{-1}$  $\zeta = 0.49388974530242$ CL Poles: s=-2.44966485404744 ± j4.31279190736033 s = -1.12228732098688 and s = -0.10078297091824

 $K_{lag} = 80/79.81139669944224 = 1.00236311239193$ 

 $\zeta$  is exact

 $K_{lag} = 0.97999709075950$  $Kv = 78.21493657490576 \text{ sec}^{-1}$  $\zeta = 0.5000000000001$ CL Poles:  $s = -2.44773023820451 \pm j4.23959313579281$ s=-1.12613839738740 and s=-0.10080112620358

**Remarks on Root Locus and Design Based on Root Locus** 

Manipulating the roots and the poles of the closed loop system may yield the desired solution, which can be sought by root locus method.

Stringent design specs. carry priority. Meeting them precisely may require computer based analysis and design.

It is useful to know the following Matlab functions: rlocus(.,.), rlocfind(.,.) and rltool. The last one lets you play with the poles and zeros to see their effects on responses and several other control engineering design tools.